

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the above-referenced application.

Listing of Claims:

Claims 1 - 9 (Cancelled)

10. (New) A plasma display apparatus, comprising:

a pull-down circuit that receives a digital telecine video signal and performs 2-3 pull-down processing thereon;

a first memory that exchanges signals with the pull-down circuit;

a sub-field development circuit that performs sub-field development based on signals from the pull-down circuit;

a second memory for sub-field development;

a plasma module that receives an output from the sub-field development circuit;

a first synchronization circuit that generates a timing at which the signal undergone 2-3 pull-down processing in the pull-down circuit is input to the first memory; and

a second synchronization circuit that generates a refresh rate at a timing of an integer-fold of a refresh rate of an original picture image,

whereby a signal is read out from the first memory at a timing of the refresh rate of the original picture image and a timing of the refresh rate from the second synchronization circuit, which are different from the timing from the first synchronization circuit, and the read-out signal is input to the second memory, and a signal is read out from the second memory at the timing of an integer-fold of the refresh rate of the original picture image supplied from said second synchronization circuit.

11. (New) The plasma display apparatus according to claim 1, wherein the refresh rate is 1/24 second and the integer-fold is three times.